Computer Architecture
EE/CS 458

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Office Hours: 1:00 PM-1:30 PM, MTWR

Prerequisite: Digital Logic EE 354 and either Computer Organization and Assembly Language Prog. CS 315 or Microprocessors EE 367.


OUTLINE
1. Introduction
2. Register Transfer Language
3. Basic Computer Organization and Design
4. Instruction Set Architecture
5. Control Unit Design
6. Pipelining
7. Computer Arithmetic and ALU Design
8. Input and Out Organization
9. Memory Organization
10. Introduction to Parallel Computer Architecture

Grading Policy for Undergraduate Students
- Midterm Exam I: 30%
- Midterm Exam II: 30%
- Final Exam: 40%

Grading Policy for Graduate Students
- Midterm Exam I: 20%
- Midterm Exam II: 20%
- Project: 20%
- Final Exam: 40%